

# Automatic Generation of Vulnerability Tests for the Java Card Byte Code Verifier

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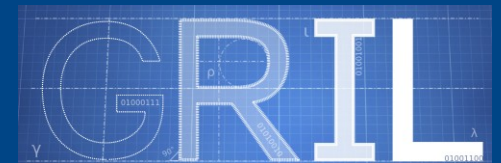
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SAR-SSI, La Rochelle

May 18<sup>th</sup>, 2011



SSD team of Jean-Louis Lanet  
Université de Limoges



Laboratory of Marc Frappier, PhD  
Université de Sherbrooke

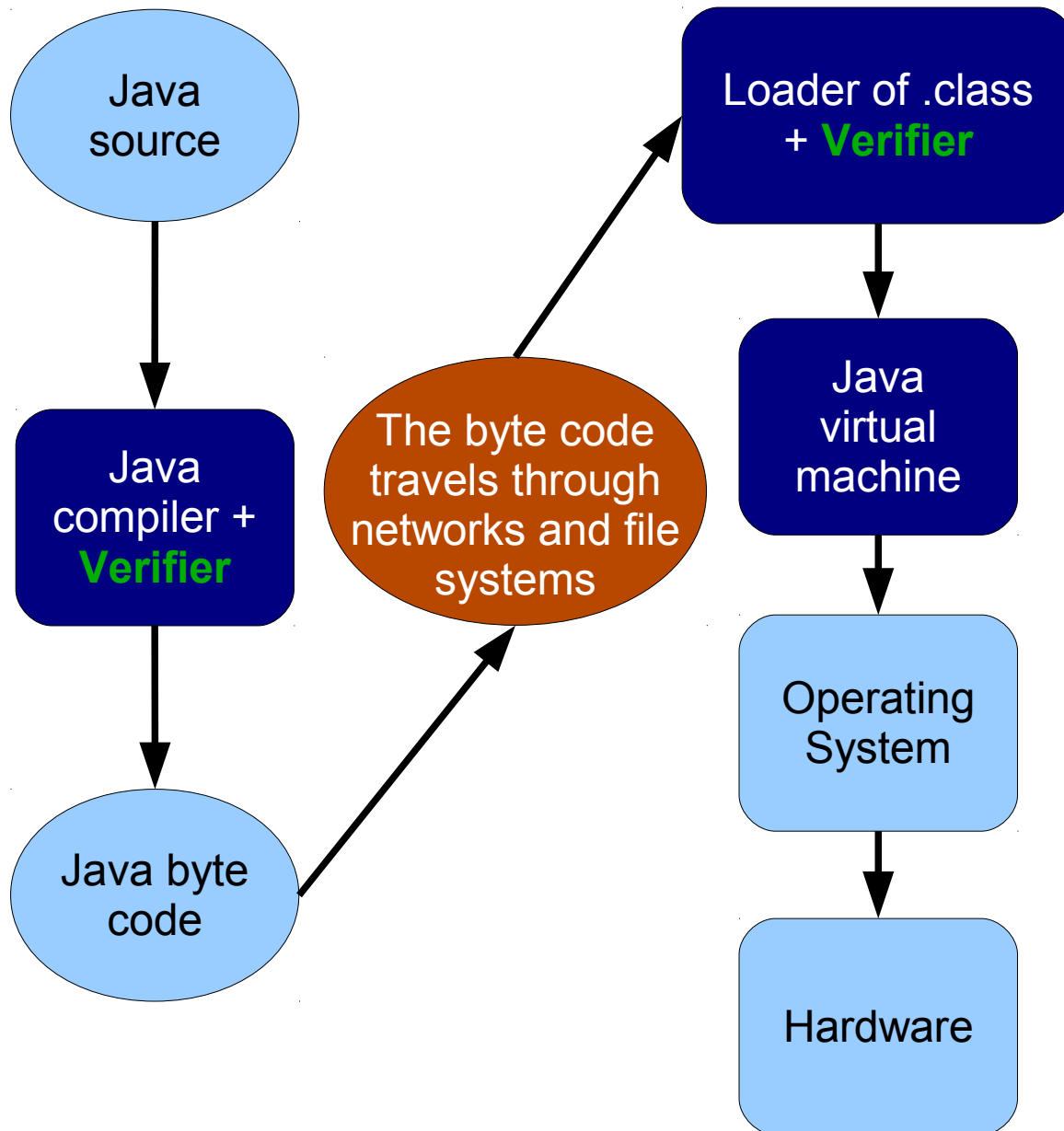
# Outline

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- Java, a strongly typed language
- Testing the implementation of the verifier
  - Definition of the vulnerability tests
  - Principle put forward
  - A few results
- Conclusions

# Java, a safe language



- Strongly typed language
- Virtual machine execution
- Sandbox
- **Byte code verifier**

# Real behavior of a byte code verifier



**Valid code  
Authorized  
execution**

**Invalid code  
Refused  
execution**

**Valid code  
Refused  
execution**

**Invalid code  
Authorized  
execution**

# Real behavior of a byte code verifier



**Valid code  
Authorized  
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**Invalid code  
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# Definition of vulnerability tests



Definition : A vulnerability test confirms that a behaviour rejected by the model will also be rejected by its implementation.

- The goal is to generate actions in order to out-pass the limits defined by the model.
- By opposition to functional tests, vulnerability tests simply test the negation of the specification.

# An example of vulnerability tests

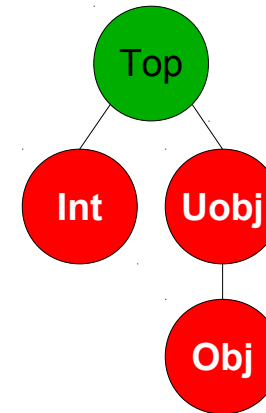


Inc :

Manipulate the integer located on top of the stack

PRE  
..., Int

POST  
..., Int



PC	Instruction	Stack
0	INIT	
1	Push0	Int
2	Inc	Int
3	Pop	
4	Halt	

# An example of vulnerability tests

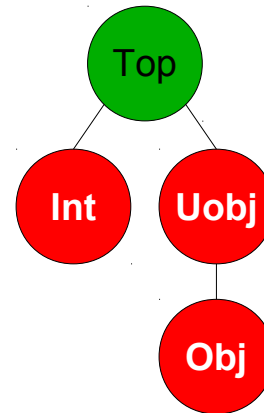


Inc :

Manipulate the integer located on top of the stack

PRE  
..., Int

POST  
..., Int



Preamble

IUT (Instruction Under Tests)

Postamble

PC	Instruction	Stack
0	INIT	
1	New	Uobj
2	Inc	<i>reject</i>
3	Pop	
4	Halt	

PC	Instruction	Stack
0	INIT	
1	New	Uobj
2	Init	Obj
3	Inc	<i>reject</i>
4	Pop	
5	Halt	



# An example of vulnerability tests

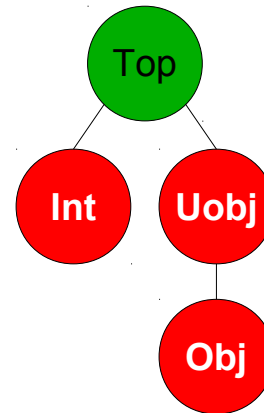


Inc :

Manipulate the integer located on top of the stack

PRE  
..., Int

POST  
..., Int



Preamble

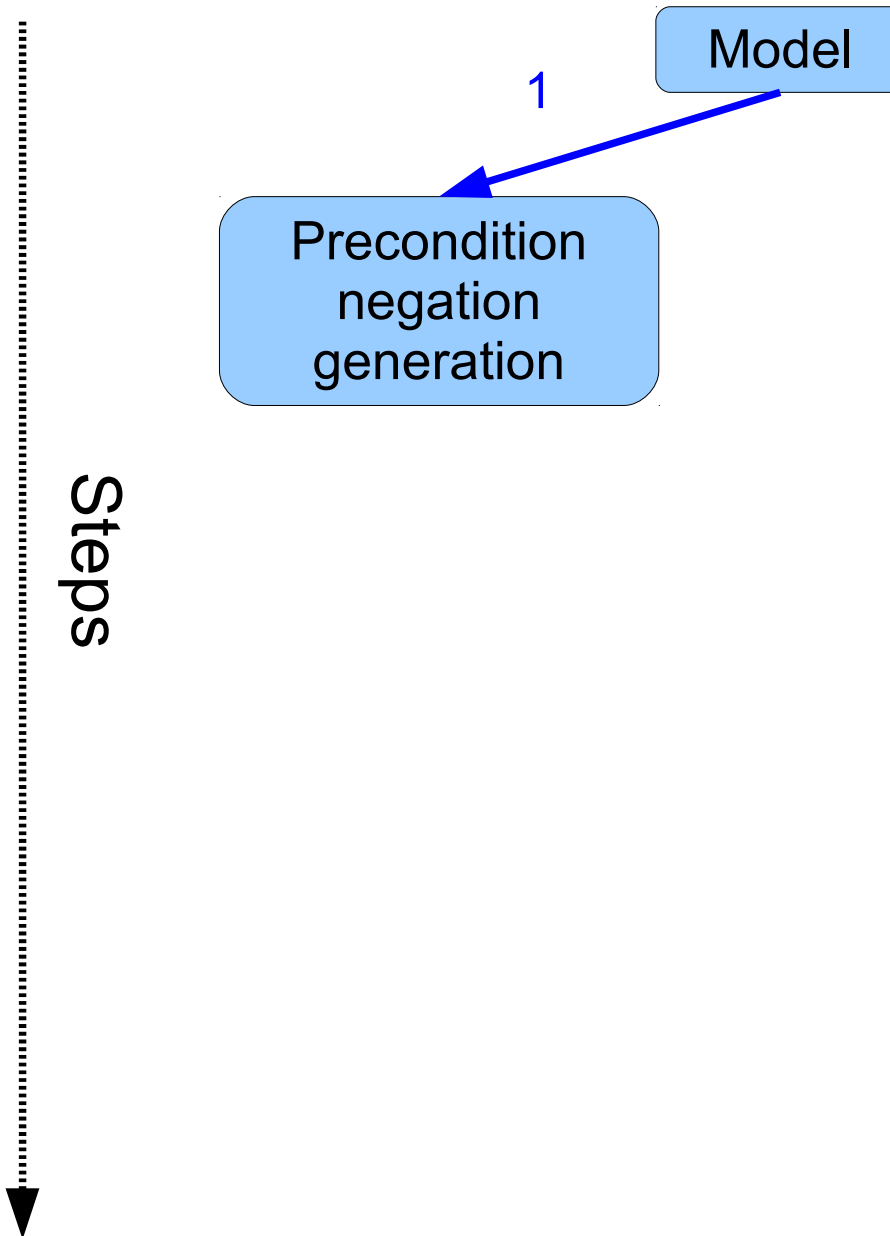
IUT (Instruction Under Tests)

Postamble

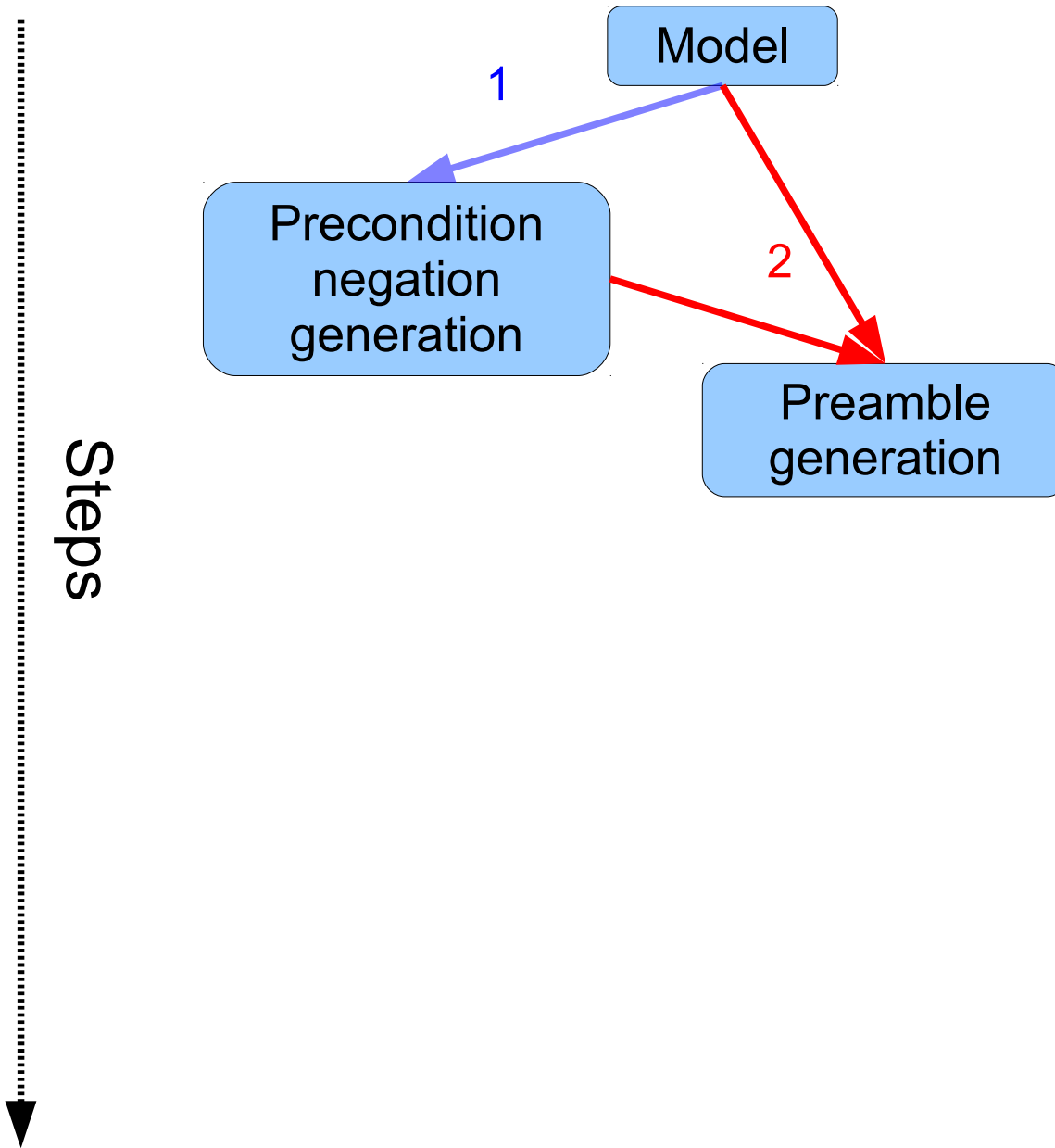
PC	Instruction	Stack
0	INIT	
1	New	Uobj
2	Inc	Obj
3	Pop	rejet
4	Halt	

- Assessment of the preamble
- Set of test cases :
  - Uobj
  - Obj
- Assessment of the postamble

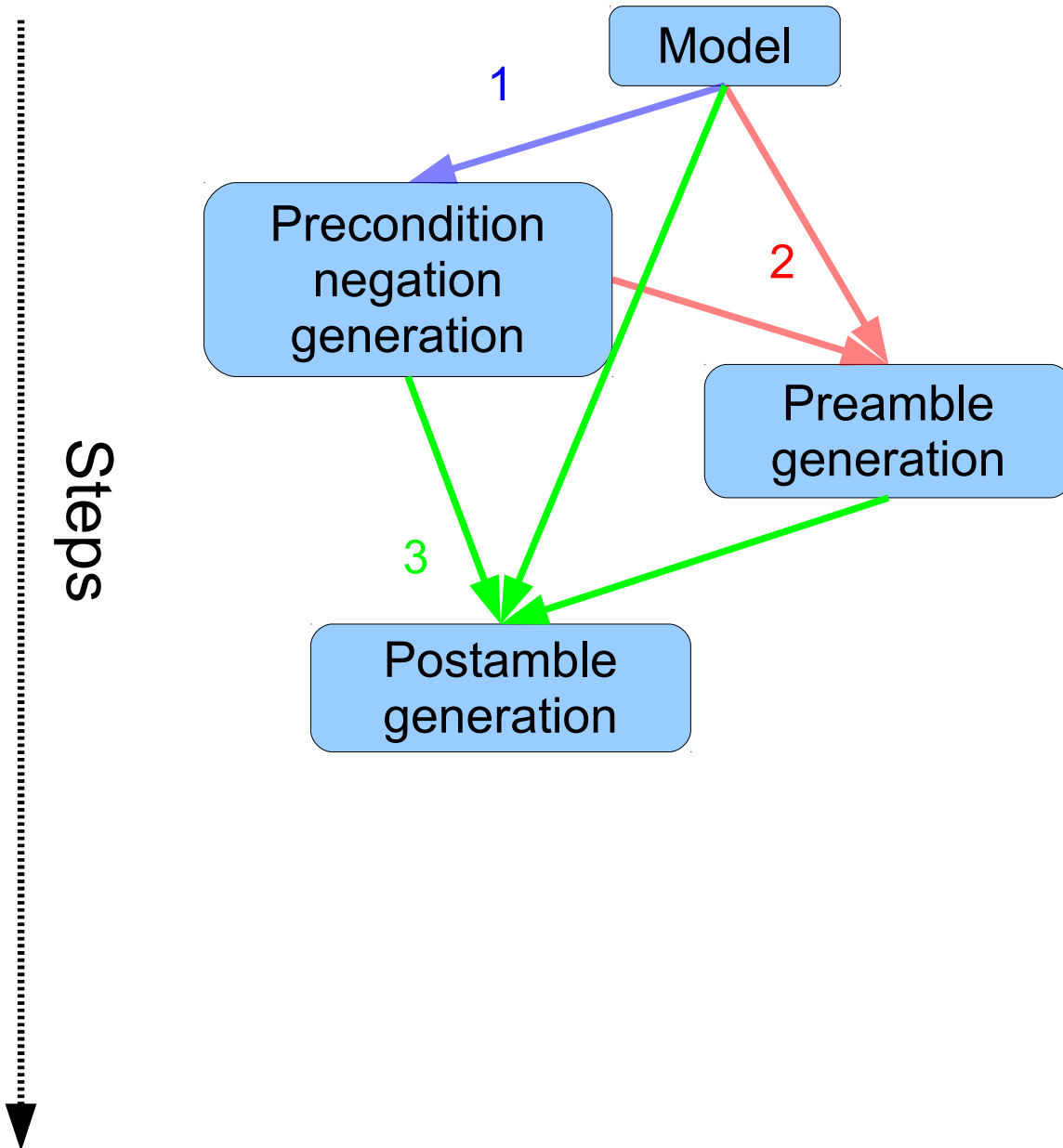
# Working mechanisms



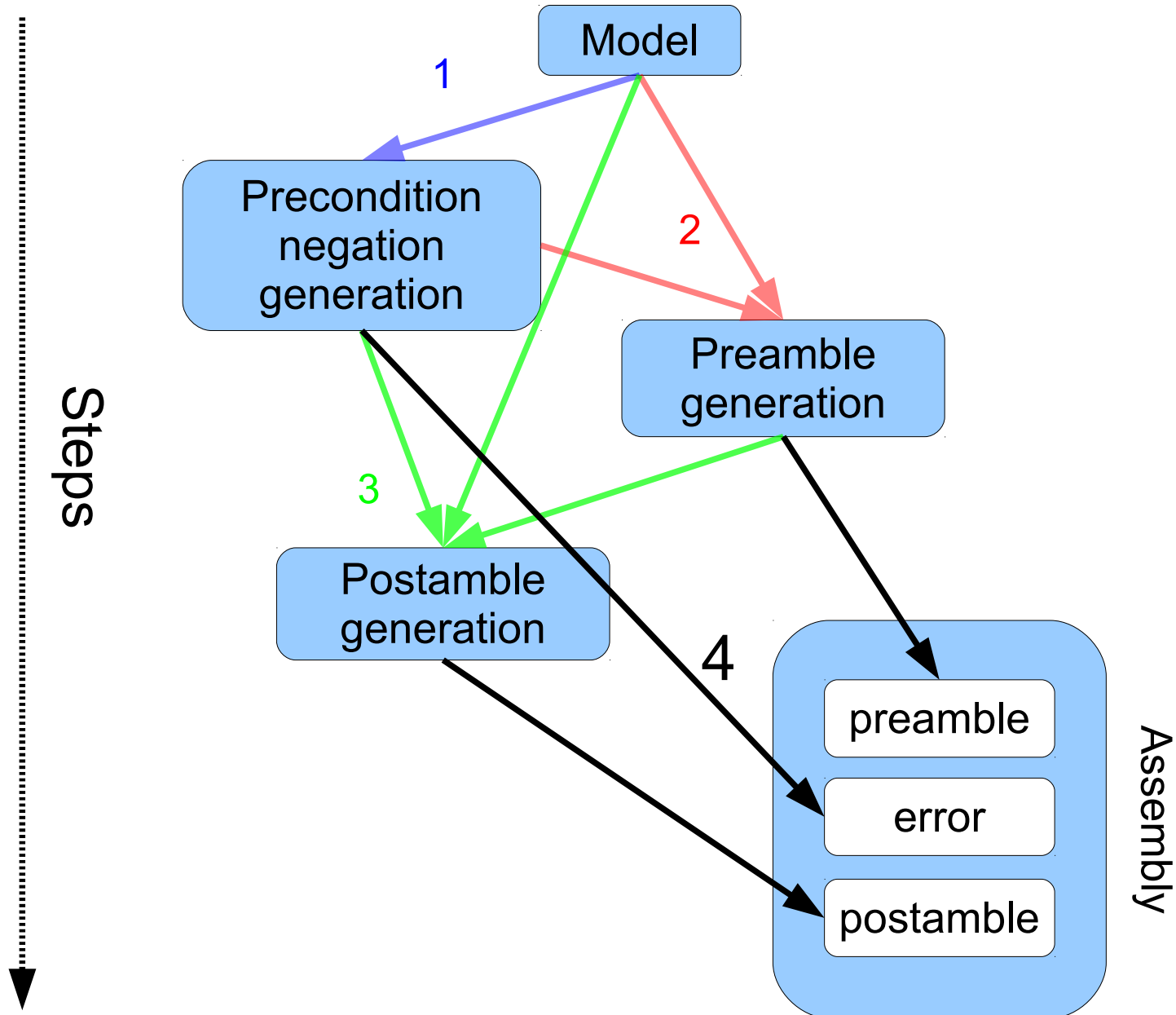
# Working mechanisms



# Working mechanisms



# Working mechanisms



# Precondition negation generation



- Grouping of clauses subsets :

$$\neg((C11 \wedge C12) \wedge C2) \begin{array}{l} \nearrow (\neg(C11 \wedge C12) \wedge C2) \\ \searrow ((C11 \wedge C12) \wedge \neg C2) \end{array}$$

- Logic transformations :

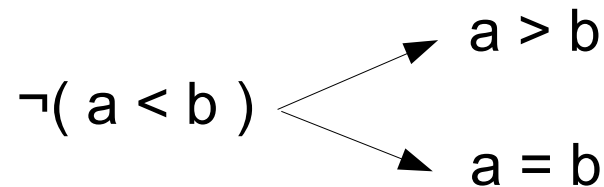
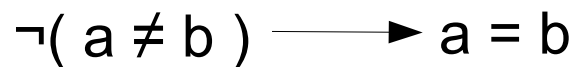
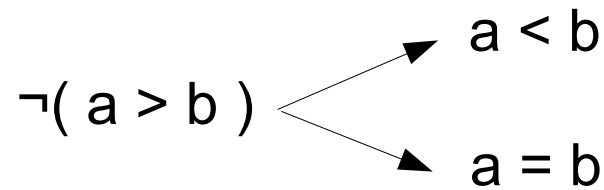
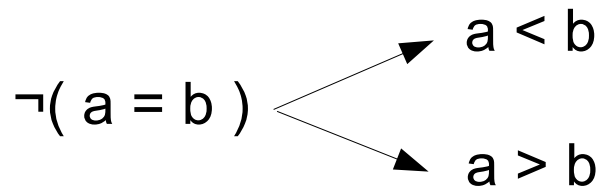
$$\neg(a \wedge b) \begin{array}{l} \nearrow (\neg a \wedge b) \\ \rightarrow (a \wedge \neg b) \\ \searrow (\neg a \wedge \neg b) \end{array}$$

$$\neg(a \vee b) \longrightarrow (\neg a \wedge \neg b)$$

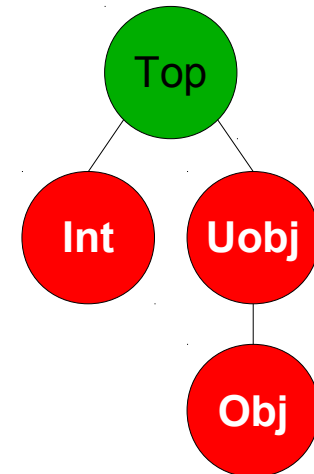
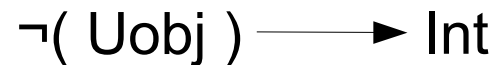
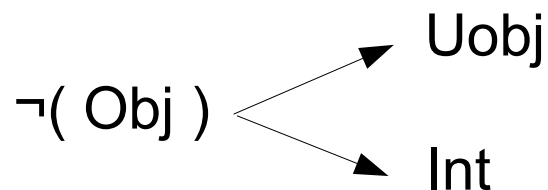
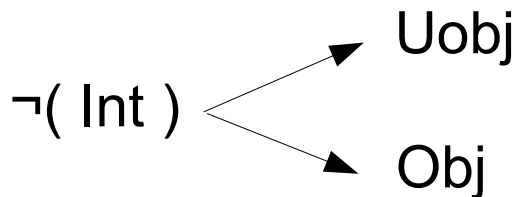
# Precondition negation generation



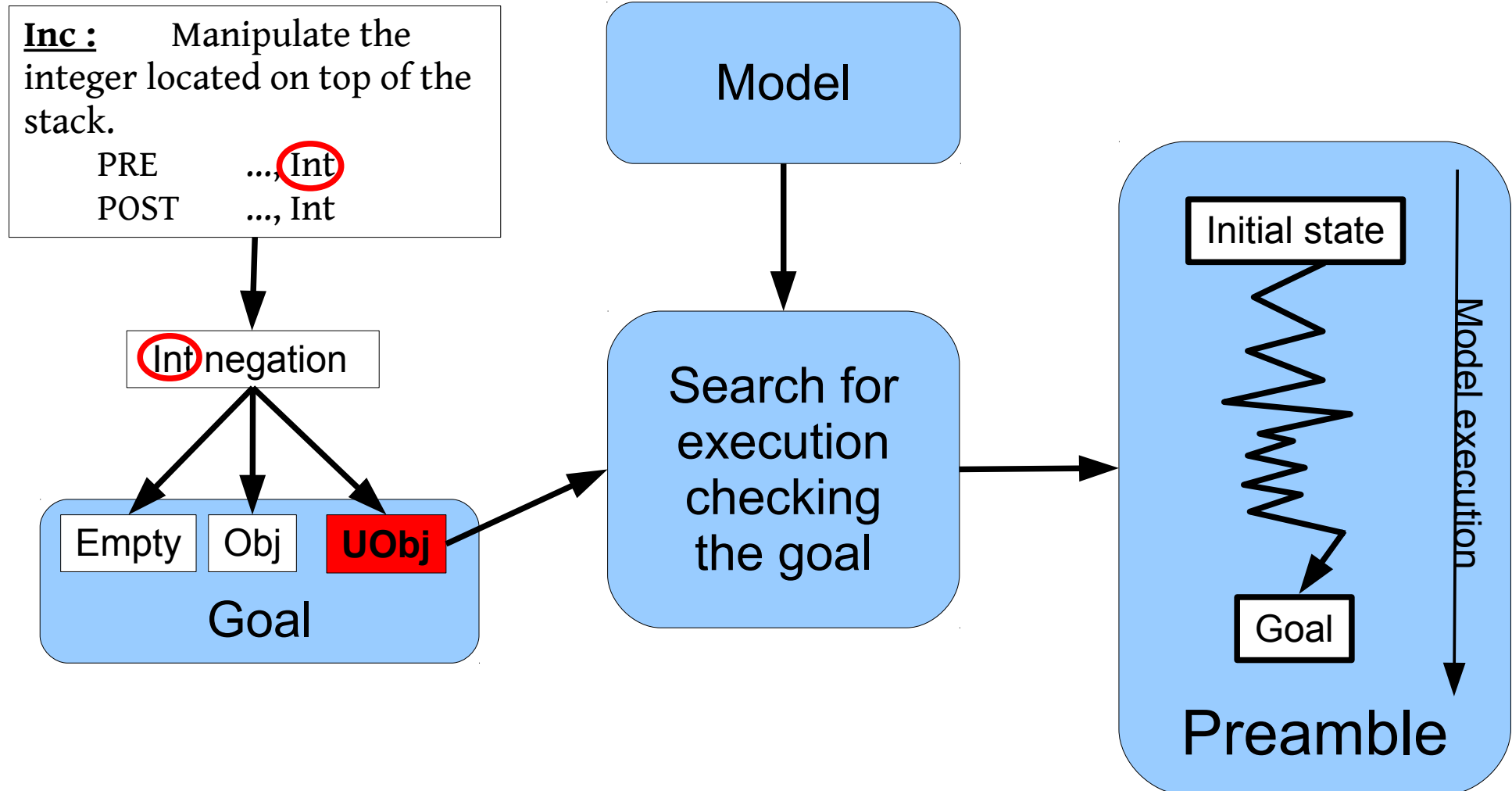
- Arithmetic transformations :



- Types transformations :

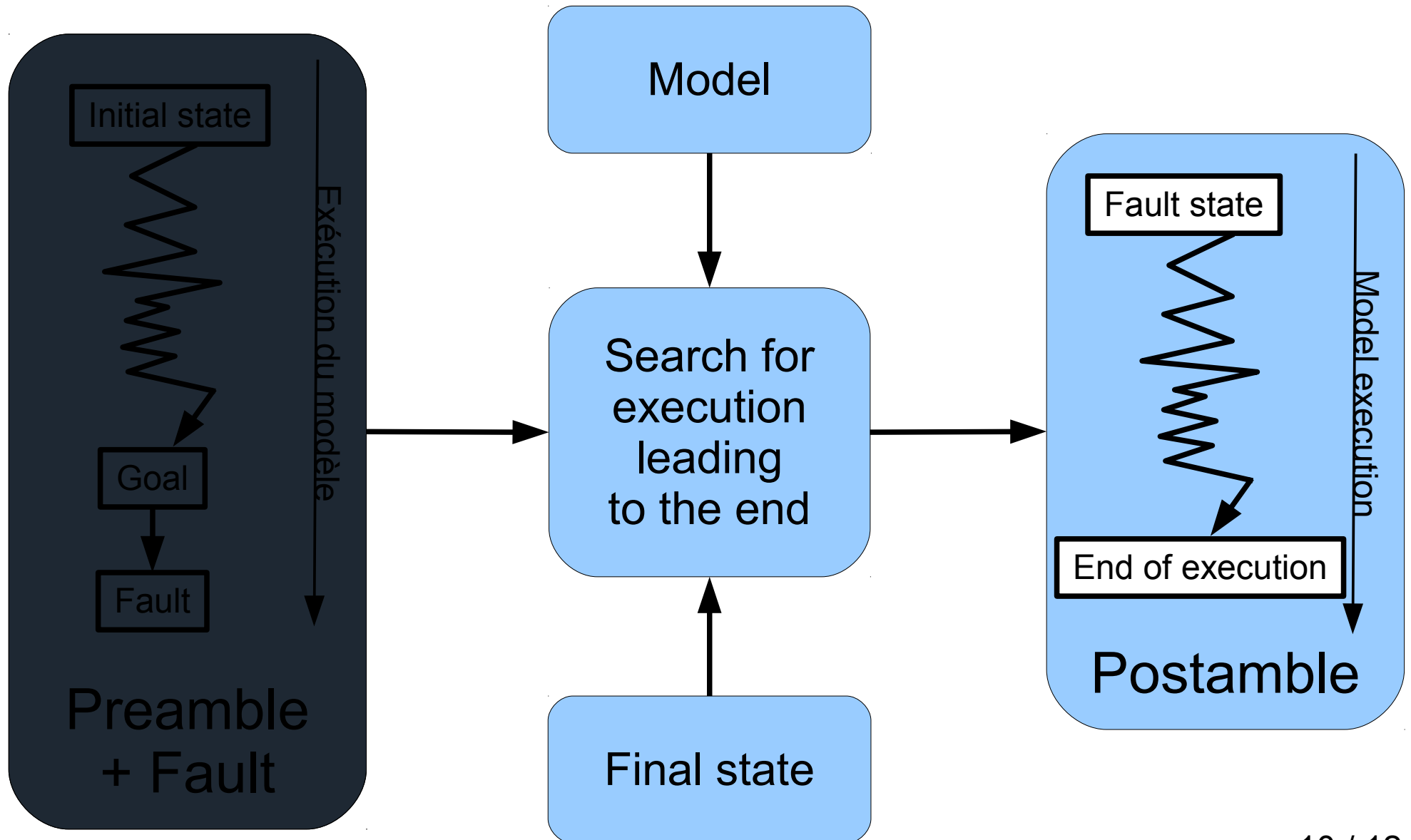


# Preamble generation

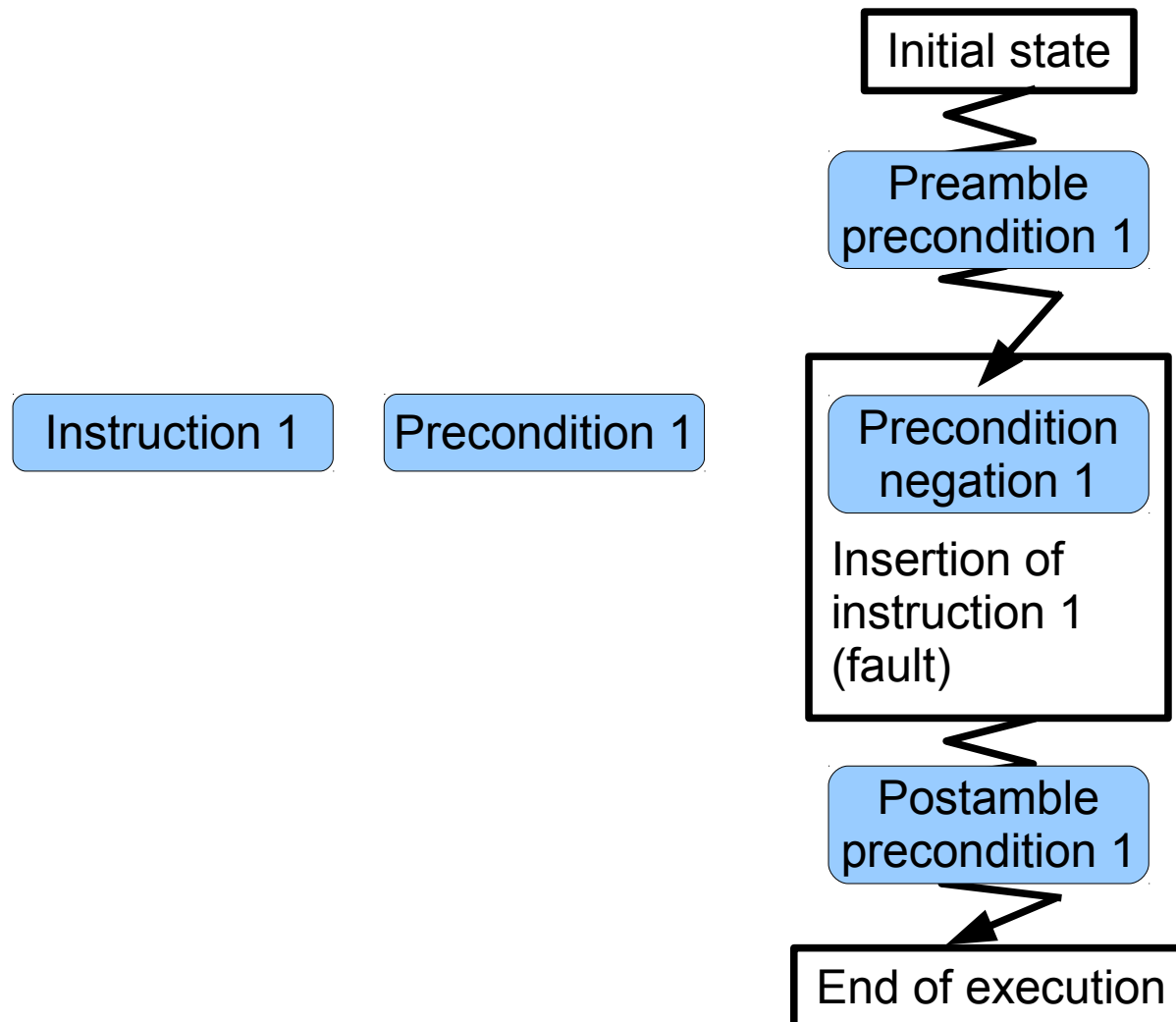




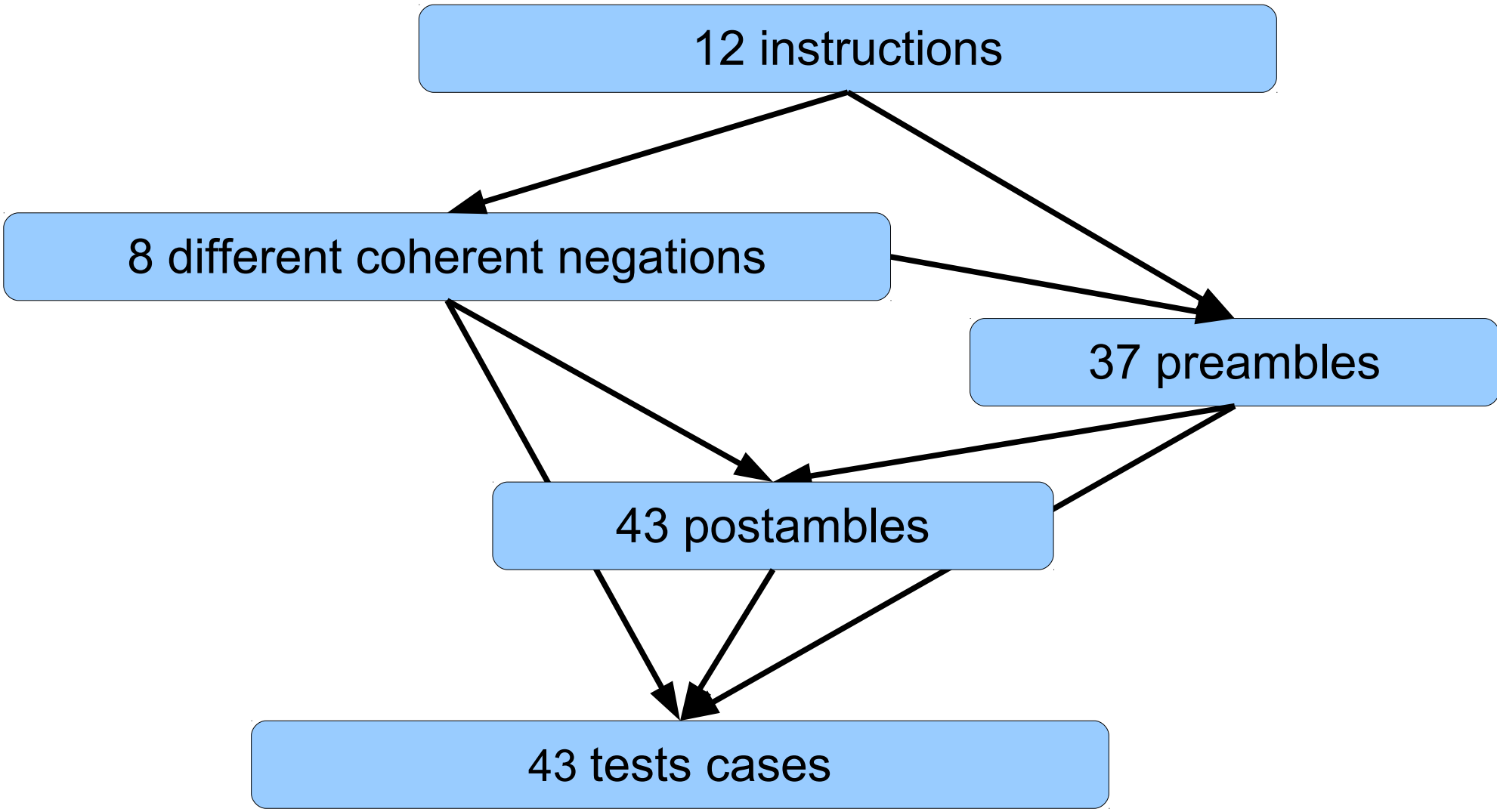
# Postamble generation



# Components assembly



# A few results...



# Conclusion and future works

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- We demonstrated the validity of our proof of concept on a reduced language set of Java
- We have been able to generate automatically the preamble and the post-amble

## Next steps

- Adapt the approach on the complete Java language
- Characterize a Java Card embedded byte code verifier
- Find and exploit vulnerabilities
- Apply it to other application domains

# Thank you !

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- Marc Frappier, PhD
- Jean-Louis Lanet
- Laboratory members
- **Founding organization**



# Contact informations

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